



UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT Gowrishankar L. GROUP ART UNIT: 2829

Chindalore et al.

APPLN. NO.: 10/609,361 EXAMINER: William M. Brewster
 FILED: June 27, 2003 CONFIRMATION No.: 8224
 TITLE: NON-VOLATILE MEMORY HAVING A REFERENCE
 TRANSISTOR AND METHOD FOR FORMING

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COMMENTS ON STATEMENT OF REASONS FOR ALLOWANCE

Commissioner for Patents
 P.O. Box 1450
 Alexandria, VA 22313-1450

Sir:

This paper is filed contemporaneous with the filing of the issue fee for this application. Entrance of the comments provided below regarding the statement of reasons for allowance provided by the Examiner is respectfully requested.

FEES

Applicants authorize the charging of Deposit Account No. 503079 (Freescale Semiconductor, Inc.) for any fees owed in connection with this application, or credit Deposit Account No. 503079 (Freescale Semiconductor, Inc.) for any refunds.

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**REMARKS**

In a communication mailed September 15, 2004, claims 1-11 were allowed. The Examiner provided reasons for allowance of the claims. Additionally, the Examiner quoted claim recitals from claims 1-6 and claims 7-11. Claim 1 was stated to be allowable because of specific claim features. In addition to the enumerated features other claim features and the combination of claim recitals are not taught or suggested by the prior art of record. Therefore, Applicants' position is that the Examiner's quotation of specific features of the independent claims was illustrative and not the only points of novelty in the allowed claims.

Respectfully submitted,

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